

RoHS
Compliant

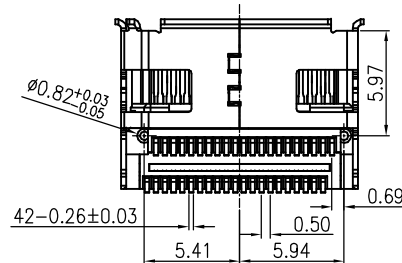
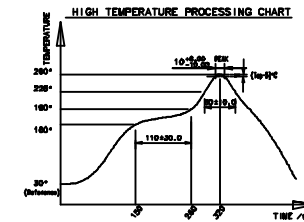
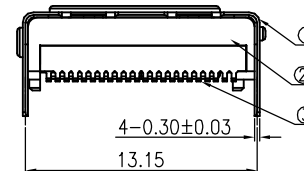
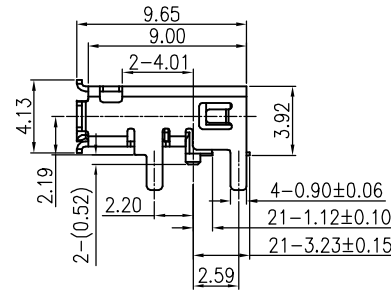
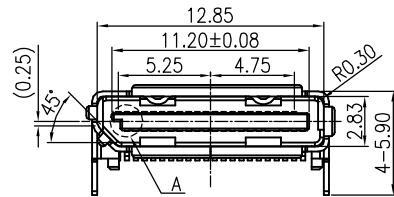
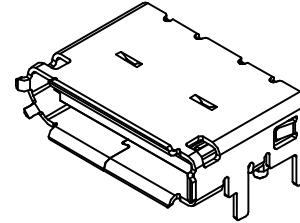
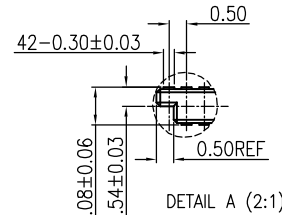
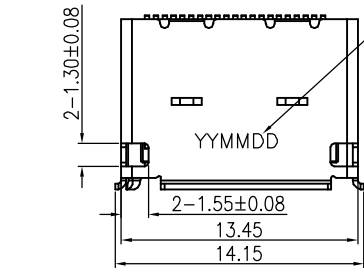
HF

REV.	ECN.NO.	MODIFY.CONTENT
X1	2021-09-03	NEW
X2	2021-10-12	印字由4码增加到5码
X3	2022-01-19	印字改为6码年月日

NOTES:

- MATERIAL:
HOUSING: HIGH TEMPERATURE THERMOPLASTIC 94V-0, BLACK COLOR
TERMINAL: COPPER ALLOY
SHELL: STAINLESS STEEL
- PLATING:
TERMINAL:
SELECTIVE Au (SEE ORDER INFORMATION) ON CONTACT AREA,
MATTE TIN ON SOLDER TAIL AREA, BOTH OVER Ni UNDERPLATED.
SHELL:
SOLDERABLE Ni PLATED OVERALL.
- MECHANICAL CHARACTERISTICS:
3-1. INSERTION FORCE: 8~40N w/ PASSIVE LATCH.
3-2. INSERTION FORCE: 8~40N w/ ACTIVE LATCH.
3-3. DURABILITY: 50 MATING CYCLES INTERNAL ONLY.
3-4. OPERATING TEMPERATURE: -25° C TO +60° C INTERNAL ONLY.
- PACKING: TAPE & REEL
- ORDER INFORMATION: C U C11FL4206XX

- 01: Au 1u"
02: Au 3u"
03: Au 15u"
04: Au 30u"



ITEM	DESCRIPTION	Q'TY	MATERIAL	FINISH
③	CONTACT	42	NI COPPER	50u" MIN. NICKEL PLATING OVERALL 50u" MIN. TIN PLATING ON SOLDER AREA PLATING ON CONTACT AREA AS PRODUCT NG MATRIX
②	HOUSING	1	LCP	UL94V-0 COLOR: BLACK
①	SHELL	1	STAINLESS STEEL	SOLDERABLE 50u"Ni PLATED OVERALL

Row offset--no pin this side	NA	2nd	B1	RESERVED	
A1	Power	2nd	1st	B2	GND
A2	GND	1st	2nd	B3	PETp0
A3	PERp0	2nd	2nd	B4	PETn0
A4	PERn0	2nd	1st	B5	GND
A5	GND	1st	2nd	B6	PETp1
A6	PERp1	2nd	2nd	B7	PETn1
A7	PERn1	2nd	1st	B8	GND
A8	GND	1st	2nd	B9	2-WIRE CLOCK
A9	RP Type	2nd	2nd	B10	2-WIRE DATA
A10	CWAKE#	2nd	1st	B11	GND
A11	GND	1st	2nd	B12	PERST#
A12	VSP	2nd	2nd	B13	CPRSNT#
A13	VSP	2nd	1st	B14	GND
A14	GND	1st	2nd	B15	PETp2
A15	PERp2	2nd	2nd	B16	PETn2
A16	PERn2	2nd	1st	B17	GND
A17	GND	1st	2nd	B18	PETp3
A18	PERp3	2nd	2nd	B19	PETn3
A19	PERn3	2nd	1st	B20	GND
A20	GND	1st	2nd	B21	Power
A21	RESERVED	2nd	NA	Row offset--no pin this side	
PIN	DESCRIPTION	MATING SEQUENCE	PIN	SIGNAL NAME	

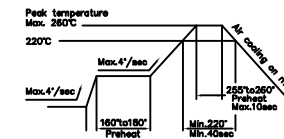
x4 FIXED INTERNAL CONNECTOR(ROOT)

x4 FIXED INTERNAL CONNECTOR(ROOT)

NOTE: SIGNAL DESCRIPTION

- PETp0-PETp3, PETn0-PETn3, PERp0-PERp3, PERn0-PERn3:
-Differential PCIe Transmitter/Receiver (4X)
- VSP: Vendor-Specific Position
- CPRSNT#: Cable Present Signal
- CWAKE#: Power management signal for downstream device wakeup events.
- 2-WIRE CLOCK: Management Bus Clock. More Info. in PCIe Oculink 1.0RC Spec.
- 2-WIRE DATA: Management Bus Data. More Info. in PCIe Oculink 1.0RC Spec.

REFLOW PROFILE:

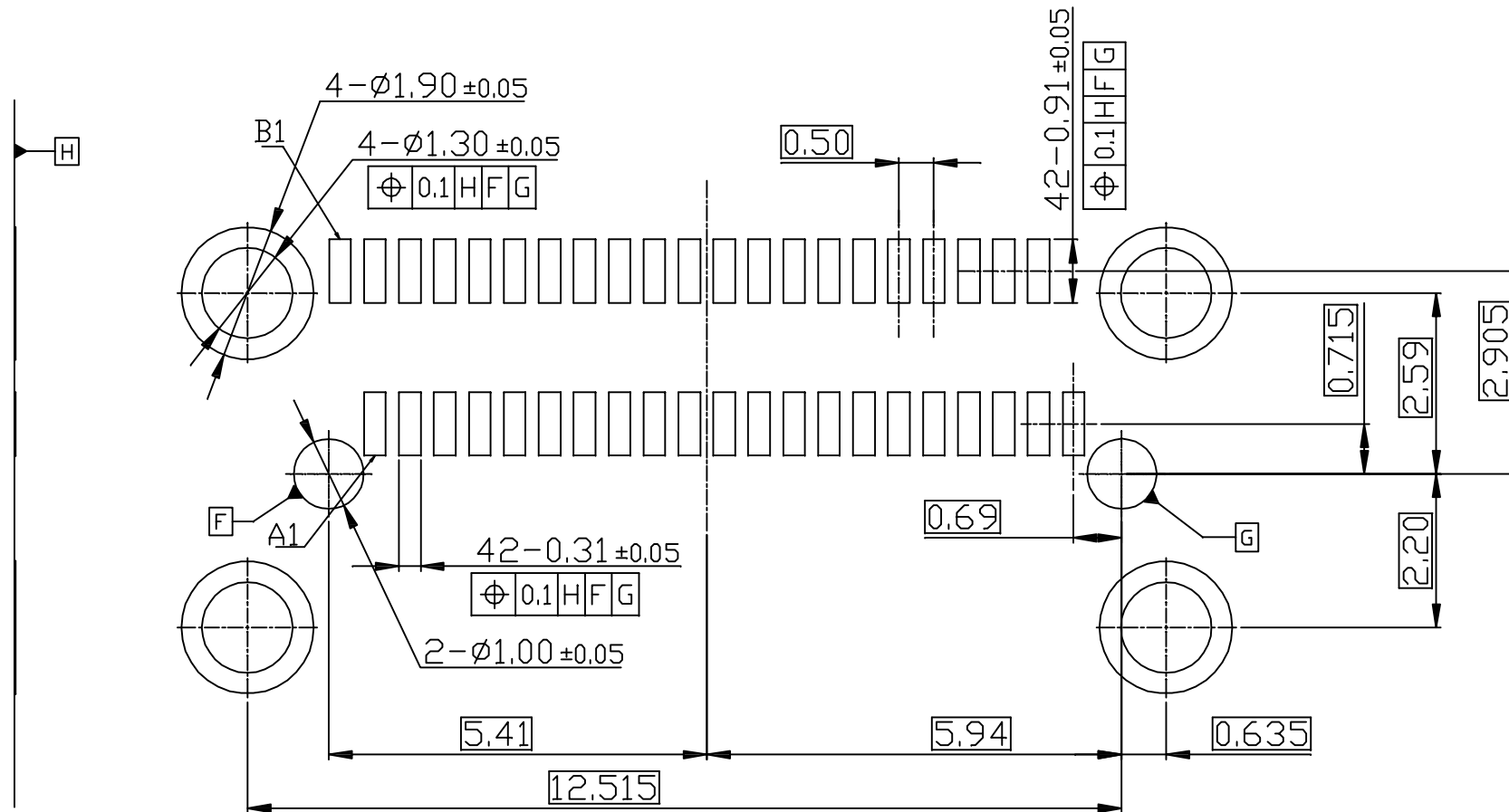


GENERAL	TOLERANCE	DWG.NO.	FL42-0600	PART.NO.	CUC11FL4206XX	DRAWN	Tony 2021-09-03	UNIT	mm	SCALE	1:1
x.±0.50	x.± 5°	REV.	X1	TITLE	Oculink 4i 42p connector	CHECKED					
.x±0.25	.x± 2°	SIZE		SHEET	1 OF 3	APPROVED					
.xx±0.15	.xx± 1°	A4									

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RECOMMENDED PCB LAYOUT

GENERAL TOLERANCE		DWG.NO.	FL42-0600	PART.NO.	CUC11FL4206XX	DRAWN	Tony 2021-09-03	UNIT	mm	SCALE	1:1
x,±0.50	x.*±5°	REV.	X1	TITLE	Oculink 4i 42p connector	CHECKED					
.x±0.25	.x.*±2°	SIZE		SHEET	2 OF 3	APPROVED					
.xx±0.15	.xx.*±1°	A4									

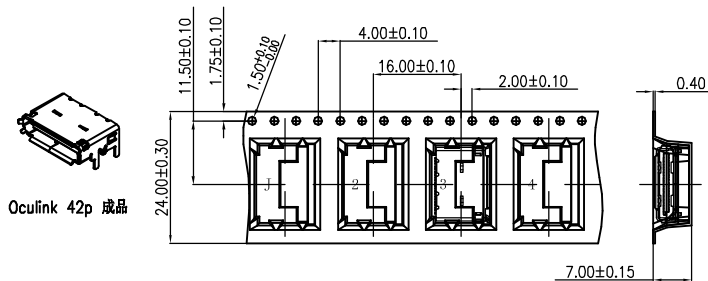
互连精密技术（深圳）有限公司
包装作业规范

REV.	ECN.NO.	MODIFY.CONTENT
X1	2021-09-03	NEW

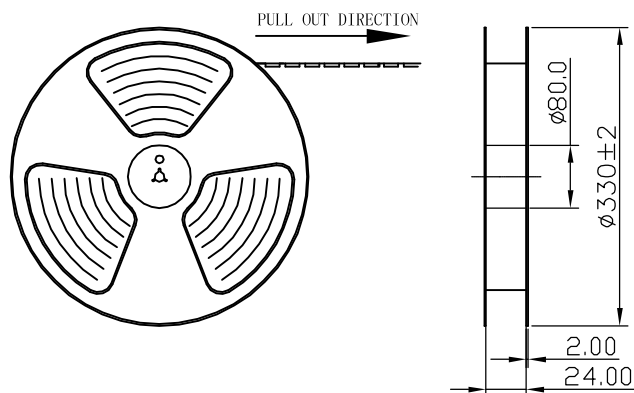
包装作业图示及说明

(PACKING OPERATION DIAGRAM & INSTRUCTION)

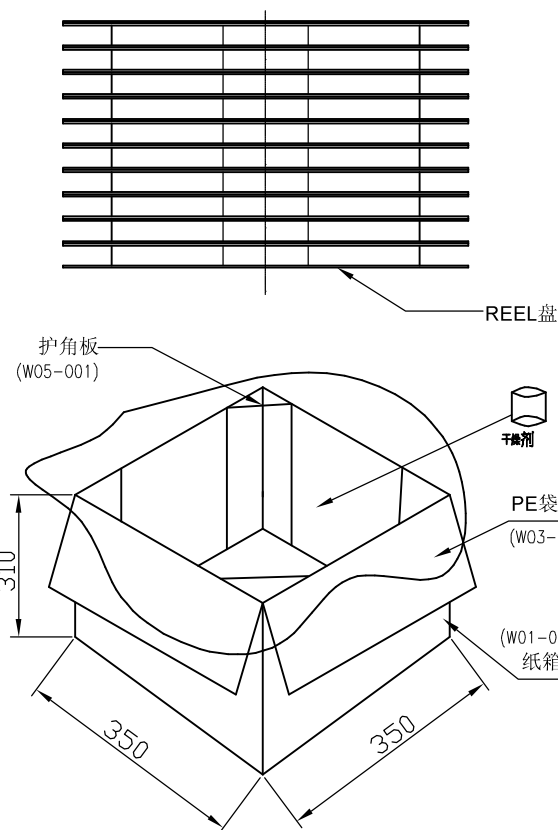
- 一. 1) 将成品一一放入REEL包装盘内,依同一方向放入.
2) 包装时,如图所示.
3) 一个REEL包装盘放置600个成品.



- 二. 1) 装盘前先把前面空15pcs产品,然后再开始装盘,
尾端也需空15pcs产品,上带加长200mm.
2) 装满成品的REEL包装盘如下图所示.



- 三. 1) 每箱装 10 盘REEL包装盘.
2) 每箱放置6000 PCS 的成品.



- 四. 1) 用TAPE將紙箱封實.

備 註 (REMARK)

1. 若有未裝滿之零數箱,必須以緩衝材塞滿.

GENERAL TOLERANCE		DWG.NO.	FL42-0600	PART.NO.	CUC11FL4206XX	DRAWN	TJNY 2022-01-18	UNIT	mm	SCALE	NO SCALE
x,±0.50	x.*± 5°	REV.	A	TITLE	Oculink 4i 42p connector	CHECKED					
.x±0.25	.x.*± 2°	SIZE		SHEET	3 OF 3	APPROVED					
.xx±0.15	.xx.*± 1°	A4									