

RoHS
Compliant

HF

REV.	ECN.NO.	MODIFY.CONTENT
X1	2022-10-12	NEW

NOTES:

1 MATERIAL:

HOUSING: HIGH TEMPERATURE THERMOPLASTIC 94V-0, BLACK COLOR

TERMINAL: COPPER ALLOY

SHELL: STAINLESS STEEL

2 PLATING:

TERMINAL:

SELECTIVE Au (SEE ORDER INFORMATION) ON CONTACT AREA,

MATTE TIN ON SOLDER TAIL 80u"AREA, BOTH OVER Ni 50u" UNDERPLATED.

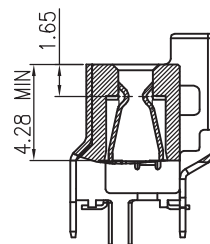
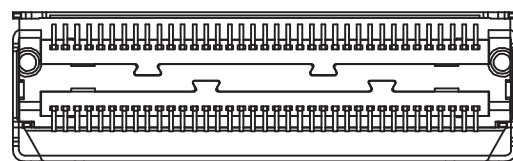
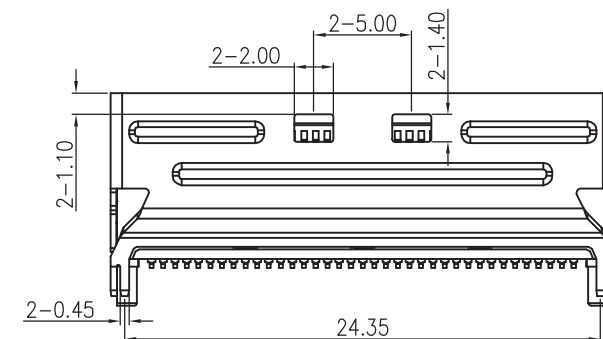
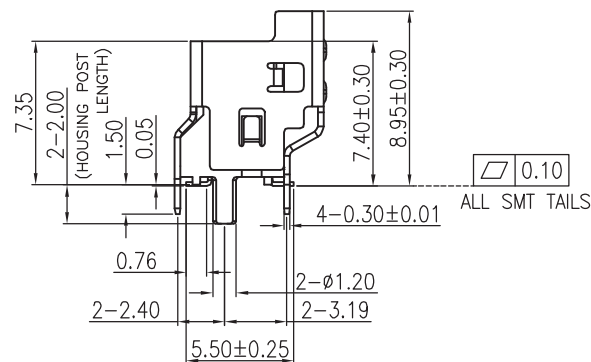
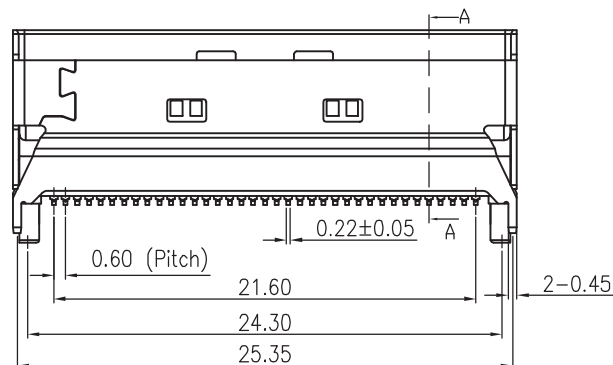
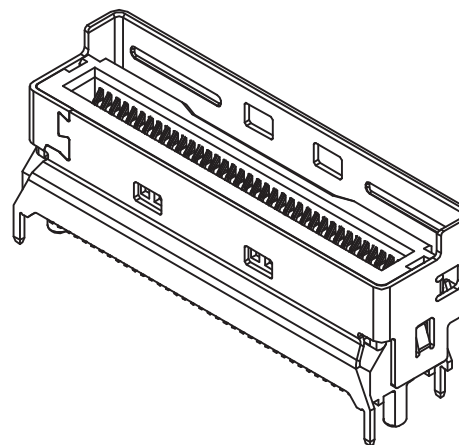
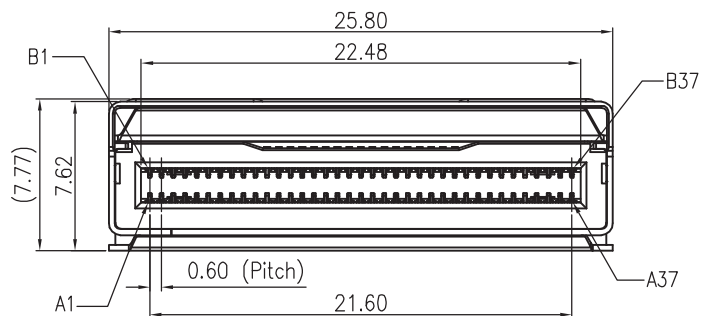
SHELL:

SOLDERABLE Ni 50u" PLATED OVERALL.

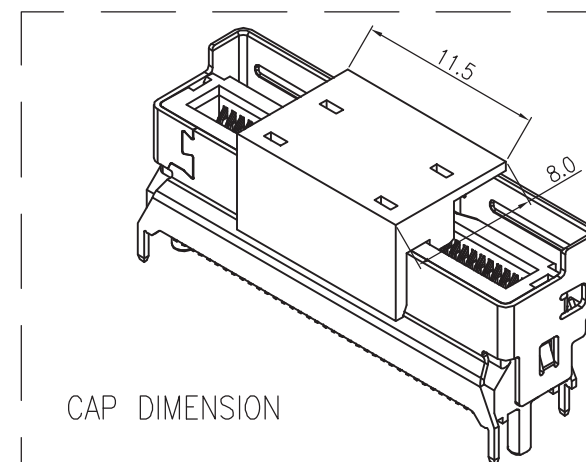
3 PACKING: TAPE & REEL

4 ORDER INFORMATION: CUC09FL7400XX

42: Au 1u"
43: Au 3u"
46: Au 15u"
47: Au 30u"



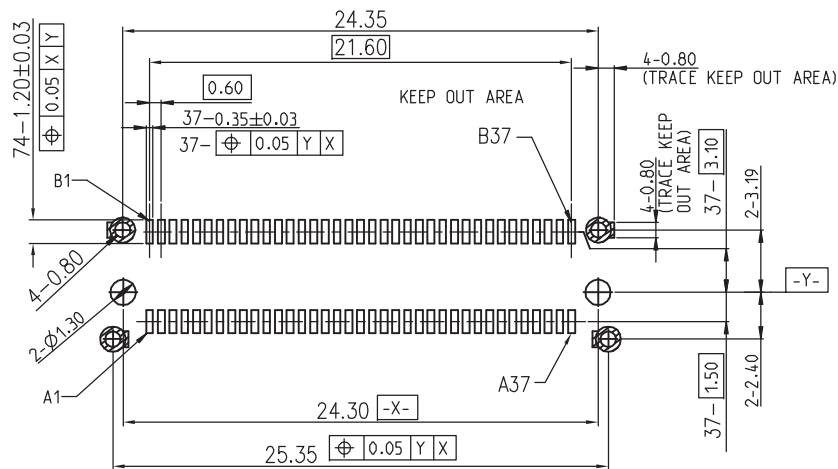
SECTION A-A



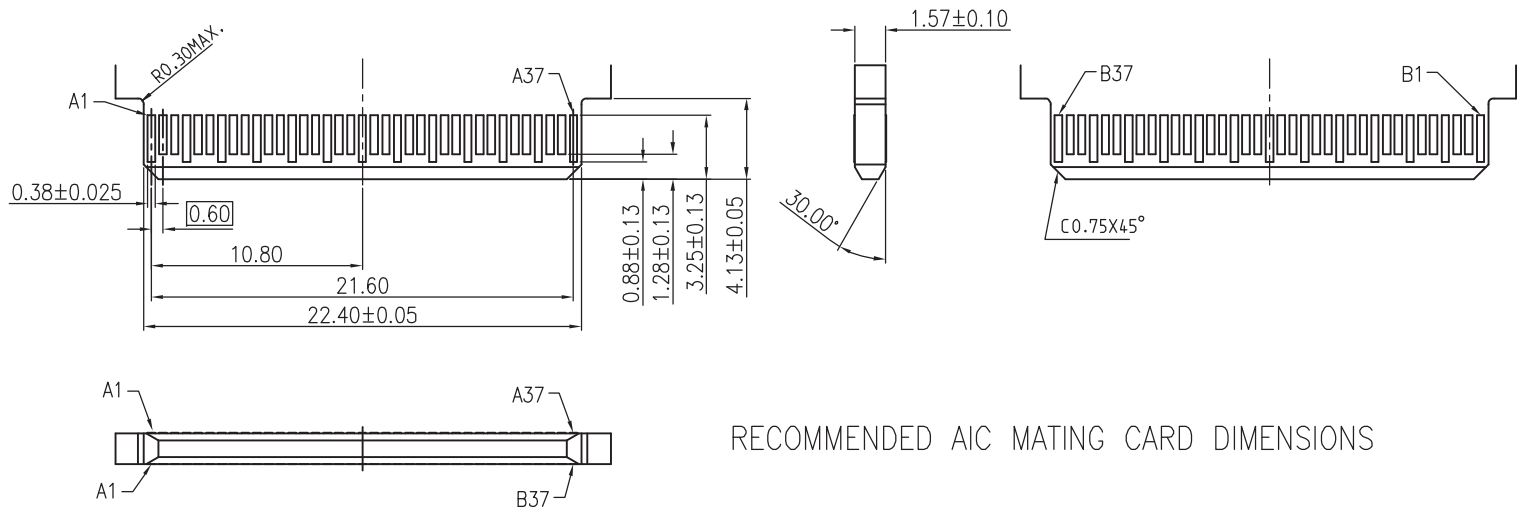
CAP DIMENSION

GENERAL TOLERANCE		DWG.NO.		PART.NO.	CUC09FL7400XX	DRAWN	Tony 2022-10-12	UNIT	mm	SCALE	1:1
x,±0.50	x.*± 5°	REV.	X1	TITLE	MINI COOL EDGE IO INTEGRATED 74P CONNECTOR 0.60PITCH (PCIE GEN 5)	CHECKED					
.x±0.25	.x.*± 2°	SIZE		SHEET	1 OF 1	APPROVED					
.xx±0.15	.xx.*± 1°	A4									

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RECOMMENDED PCB LAYOUT
(GENERAL TOLERANCES: +/−0.05mm)



RECOMMENDED AIC MATING CARD DIMENSIONS

GENERAL TOLERANCE		DWG.NO.	X1	PART.NO.	CUC09FL7400XX	DRAWN	Tony 2022-10-12	UNIT	mm	SCALE	1:1
x,±0.50	x.*± 5°	REV.		TITLE	MINI COOL EDGE IO INTEGRATED 74P CONNECTOR 0.60PITCH (PCIE GEN 5)	CHECKED					
.x±0.25	.x.*± 2°	SIZE		SHEET	2 OF 3	APPROVED					
.xx±0.15	.xx.*± 1°	A4									

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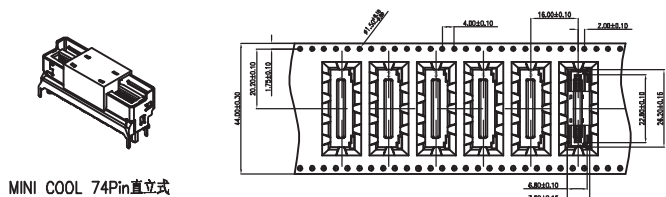
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包裝作業規範

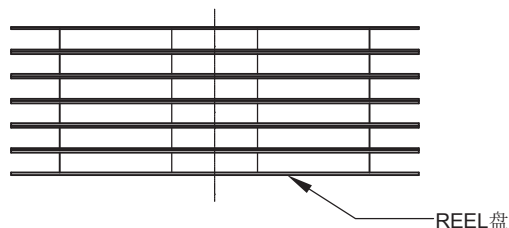
包裝作業圖示及說明

(PACKING OPERATION DIAGRAM & INSTRUCTION)

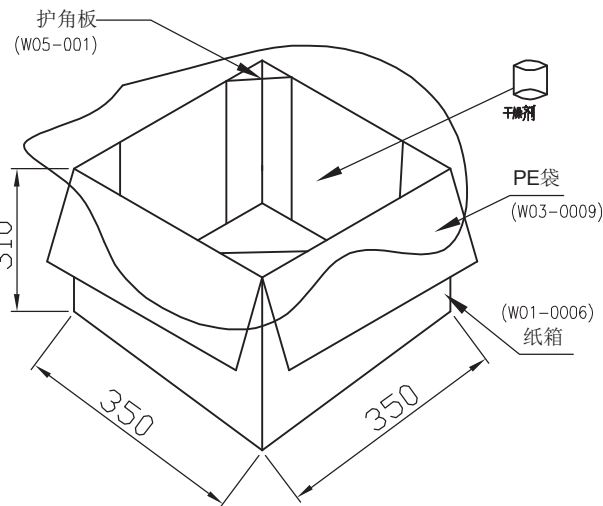
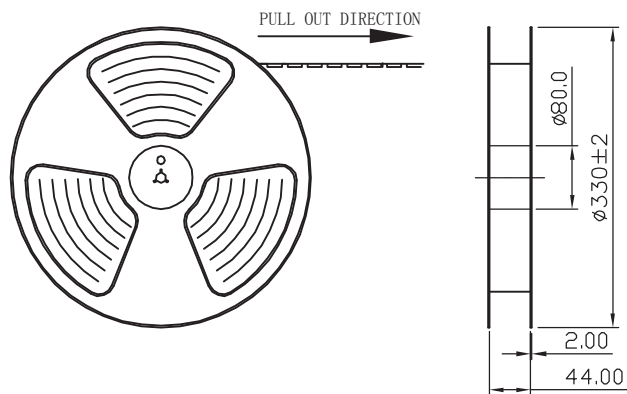
- 一. 1) 將成品一一放入REEL包裝盤內,依同一方向放入.
2) 包裝時,如圖所示.
3) 一個REEL包裝盤放置300個成品.



- 三. 1) 每箱裝 6 盤REEL包裝盤.
2) 每箱放置1800 PCS 的成品.



- 二. 1) 裝盤前先把前面空18pcs產品,然後再開始裝盤,尾端也需空18pcs產品,上帶加長200mm.
2) 裝滿成品的REEL包裝盤如下圖所示.



- 四. 1) 用TAPE將紙箱封實.

備 註 (REMARK)

1. 若有未裝滿之零數箱,必須以緩衝材塞滿.

GENERAL TOLERANCE

DWG.NO.

PART.NO.

CUC09FL7400XX

DRAWN

Tony 2022-10-12

UNIT

mm

SCALE

1:1

x,±0.50

x.± 5°

REV.

X1

TITLE

MINI COOL EDGE IO INTEGRATED 74P
CONNECTOR 0.60PITCH (PCIE GEN 5)

CHECKED

SHEET

3 OF 3

APPROVED

CONSHARE